PROJECT REPORT

ON

HALF ADDER AND FULL ADDER

Submitted to

Govt.NehruP.G.CollegeDongargarh(C.G.) In partial fullfillment of requirement for

> The award of degree OF

MASTER OF SCIENCE IN PHYSICS



MANSI MANDLOI

UNDER **SUPERVISION** OF

Mr.Roshan Kumar

DEPARTMENT OF PHYSICS



AFFILIATED TO HEMCHAND YADAV UNIVERSITY, DURG (C.G.)

SESSION 2023-2024



DECLARATION

We undersigned solemnly declare that the report of the project work entitled "HALF ADDER AND FULL ADDER" is based on my work carried out during the course of my study under the supervision of

Mr.Roshan Kumar

(Assistant Professor, Department of Physics)

We assert that the statement made and conclusions drawn are the outcome of the project work. We further declare that to the best of my knowledge and believe, it does not contain any part of work which has been submitted for the award of any on degree/ diploma/ certificate in this college.

Name: MANSI MANDLOI

Roll No. 235070850010



CERTIFICATEBYTHEEXAMINERS

This is to certify thattheprojectwork entitled:

HALF ADDER AND FULL ADDER

Submittedby

MANSI MANDLOI Roll No. 235070850010

asapart of the examination for the award of Master of Science Degree in Physics in Govt. Nehru P.G.College, Dongargarh(C.G.) has been examined by the undersigned.

Roman

InternalExaminer

Date:

ExternalExaminer

Date: 9/5/24,



CONTENTS

.NO.	DESCRIPTION
1	Abstract
2	Introduction
3	Basic Principle
4	ComponentsRequired/Values
5	Specification
6	Characteristics
7	Applications
8	Working
9	Procedure
10	Advantages
11	Disadvantages
12	Conclusion

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ABSTRACT

This project enhances the output of a Half adder & full adder each are made by using 3 IC, jumper wires toconnect breadboard and give to output in terms of sum and carry. In this project wemade Half adder using IC 7486(XOR Gate) & IC 7408 (ANDGate) & full adder using IC 7486(XOR), IC 7408 (AND) & IC 7432(OR). This report material & knowledge used to create a proof of Concept for a SUM AND CARRY using circuit. Here we use, wires to connect the required IC's pins with input pins &outputpins,Battery, resistance, Breadboard & LEDlights.

[6]

INTRODUCTION

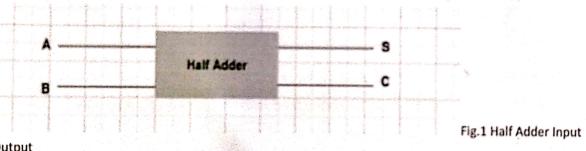
WhatisHALF ADDER AND FULL ADDER..?

Introduction

Half Adder

Half Adder is a digital circuit to calculate the arithmetic binary addition of two single-bit numbers. It is a circuit with two inputs and two outputs.

For two single-bit binary numbers A and B, half adder produces two single-bit binary outputs S and C, where S is the Sum and C is the carry.

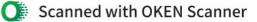


Output

LAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVILLAUVI

The sum is for the least significant bit (LSB) and carry is for the most significant bit (MSB).

Figure-1:Circuitdiagramofhalfadder



BASICPRINCIPLE

Half adder is a combinational arithmetic circuit that adds two numbers and produces a sum bit (s) and carry bit (c) both as output. The addition of 2 bits is done using a combination circuit called a Half adder. The input variables are augend and addend bits and output variables are sum & carry bits

Full Adder is the adder which adds three inputs and produces two outputs. The first two inputs are A and B and the third input is an input carry as C-IN. The output carry is designated as C-OUT and the normal output is designated as S which is SUM..



<u>COMPONENTSPECIFICATI</u> <u>ON</u>

NowSpecific details of the above components

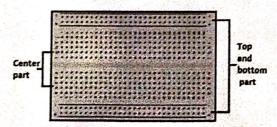
1. BREADBOARD

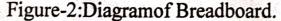
A Breadboard is simply a board for prototyping or building circuits on. It allows youtoplace components and connections on the board to make circuits without soldering.

The holes in the breadboard take care of your connections by physically holding onto parts orwireswherewe put them and electrically connecting theminside the board.

The ease of use and speed are great for learning and quick prototyping of simple circuits. Morecomplexcircuitsandhigh frequencycircuitsareless suited tobread boarding.

Breadboardcircuitsarealsonotidealforlongtermuselikecircuitsbuiltonper board(protoboard)orPCB(printedcircuitboard),buttheyalsodon'thavethe soldering(protoboard),ordesign and manufacturing costs (PCBs).

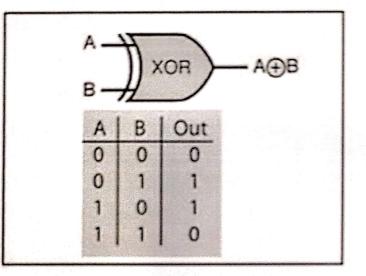




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Figure-3:CircuitDiagramof7486-IC.

2.2 Output of7486-IC:-



2.3 Features:-

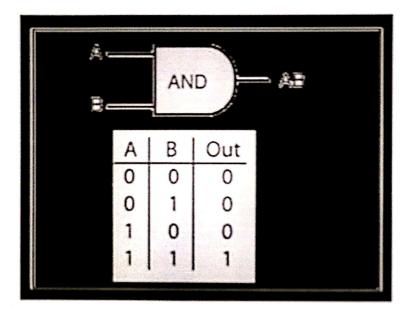
- HighNoise Immunity:VNIH = VNIL= 28% VCC. .
- PowerDown ProtectionProvided onInputs. .
- BalancedPropagationDelays. .
- Designedfor 2 V to 6V Operating Range. .
- LowNoise: VOLP = 0.8 V (Max).
- PinandFunctionCompatiblewithOtherStandard LogicFamilies. .

2.4 Specifications:-

- Type: Quad
- Material:Plastic
- Numberof: 14Pins
- Mounting:Through Hole

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3.2 Output of7408-IC:-



3.3 Features:-

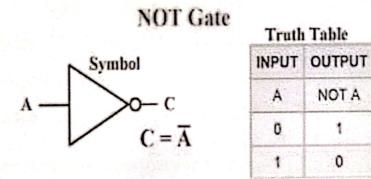
- . 4AND gates in a 14-PinDIPPackage.
- OutputsDirectlyInterface to CMOS, NMOSand TTL.
- LargeOperating VoltageRange.
- WideOperatingConditions. .

3.4 Specifications:-

- Type:DIP Bipolar .
- VoltageRating:2.0to6.0V ٠
- TemperatureRating: Oto70Deg C .
- Numberof: 14Pins
- Mounting: Through Hole

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5.1 Output of7486-IC:-



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5.2 Features:-

- Wide Operating Conditions6 HexInverters in a14-Pin DIP Package. •
- OutputsDirectlyInterface to CMOS, NMOSand TTL. ٠
- LargeOperating VoltageRange. .
- WideOperatingConditions. •

5.3 Specifications:-

- Type:DIP Bipolar .
- VoltageRating: 4.75to 5.25V
- CurrentRating:Max.16mAOutput .
- TemperatureRating: 0to70Deg C. .
- Numberof: 14Pins
- Mounting: Through Hole

6. JUMPERWIRES

Jumper wires are simply wires that have connector pins at each end, allowing them to be used to connect two points to each other without soldering. Jumper wires are typically used withbreadboards and other prototyping tools in order to make it easy to change a circuit as needed.Fairlysimple. Infact, itdoesn'tgetmuchmorebasicthan jumperwires.

A. WhatDo the Colors Mean?

Thoughjumperwirescomeinavarietyofcolors, the colors don't actually mea nanything. This means that a red jumper wire is technically the same as a black one. But the colors can be used to your advantage in order to differentiate between types of connections, such as ground or power.



Figure-8:Diagram-AofJumperwire.



C. HowaBattery Works:-

Battery is used to generate electricity. Electricity is produced by the movement of electronsthrough a conductive path. So the path followed by the electron is called a circuit. A batteryconsists of an anode, cathode, and electrolyte. As an anode is negatively charged, it means itconsists of extra electrons from the cathode. So, electrons from the anode flow towards thecathode as shown in the following picture. Due to the flow of electrons from one electrode toanotherelectrode, anelectric currentis produced.





APPLICATION

- 1. The half adder can be applicable in the creation of complete adder combinations.
- 2. Full adders are useful in Arithmetic Logic Unit (ALU) systems.
- 3. The binary addition property of half adders is applicable in the working of calculators.
- 4. Full adders are helpful in various forms of digital circuits and digital electronics.
- 5. The adders are also helpful in carrying out multiplication.
- 6. Full adders are applicable in the generation of memory addresses and the creation of

program counterpoints.

- The Full adders are essential in creating complex circuits capable of adding numerous bits simultaneously.
- 8. Full adders are critical for creating the graphical processing unit (GPU).
- 9. Half-adders are helpful in digital measuring devices.



FULL ADDER

Full Adder is the adder that adds three inputs and produces two outputs. The first two inputs are A and B and the third input is an input carry as C-IN. The output carry is designated as C-OUT and the normal output is designated as S which is SUM. The C-OUT is also known as the majority 1's detector, whose output goes high when more than one input is high. A full adder logic is designed in such a manner that can take eight inputs together to create a byte-wide adder and cascade the carry bit from one adder to another. we use a full adder because when a carry-in bit is available, another 1-bit adder must be used since a 1-bit half-adder does not take a carry-in bit. A 1-bit full adder adds three operands and generates 2-bit results.

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Conclusion

A half adder adds two single-bit binary numbers, while a full adder can also handle borrows and carries between bits. -Half adders are used in the early stages of digital electronics design, whereas full adders form the backbone of more complex systems -The simplicity of half adders makes them preferable for low-power or space-constrained applications In this article, we have discussed the differences between a half adder and a full adder. We have seen that the main difference between these two types of adders is that the half adder can only add two operands, whereas the full adder can add three operands. We have also seen that the half adder has a simple hardware architecture, while the full adder has a complex hardware architecture. Finally, we have seen that the half adder is used in digital circuits, while the full adder is used in ALUs, CPU registers, and memory units.

AFFILIATED TO HEMCHAND YADAV UNIVERSITY, DURG (C.G.) SESSION 2023-2024



DEPARTMENT OF PHYSICS

Mr.Roshan Kumar

SUPERVISION OF

UNDER

Rupesh Patel





Govt.Nehru P.G. College Dongargarh (C.G.) In partial fulfillment of requirement for

Submitted to

The award of degree

MASTER OF SCIENCE

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IN PHYSICS

Automatic Solar Tracker System

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PROJECT REPORT

CERTIFICATE This is to certify that the report of the project work entitled "Automatic Solar Tracker" carried out by Rupesh Patel bearing Roll no. 235070850016 and Enrollment No. HU/502/19007202 under my guidance and physics Department of Govt.Nehru p.g.college. Dongargah (C-G), India: To the best of my knowledge the report embodies the work of the candidate him/herself has duly been completed. Mutar Mutar Mr. Roshan Rumar Dr (mt.) EV. Revay

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Conclusion	12
Disadvantages	
Advantages	10
Procedure	9
Working	8
Applications	7
Characteristics	6
Specification	S
Components Required/Values	Ą
Basic Principle	ω
Introduction	2
Abstract	
Description	S.No.
CONTENTS	



INTRODUCTION :- In remote areas the sun is a cheap source of electricity because instead of hydraulic generators it uses solar cells depends on the intensity of sunlight and the angle of incidence. It means to get maximum efficiency, the solar panels1 must remain in front of sun during the whole day. But due to rotation of earth those panels can't maintain their position always in front of sun. This problem results in decrease of their efficiency. Thus to get a constant output, an automated system is required which should be capable to constantly rotate the solar panel. The Automatic Solar Tracking System (ASTS) was made as a prototype to solve the problem, mentioned above. It is completely automatic and keeps the panel in front of sun until that is visible. The unique feature of this system is that instead of take the earth as in its reference, it takes the sun as a guiding source. Its active sensors constantly monitor the sunlight and rotate the panel to wards the direction to the rotation of earth. But its peed of rotation is same as that of earth's rotation. Due to its property when after some me e.g. half an hour when the sun again gets visible, the solar panel is exactly in front of sun: Moreover the system an manage the errors and also provides the error messages n the LCD display. In manual mode, through the software at pmputer, the solar panel can be rotated at any desired angle.

4 Jane David

4. SOLAR SENSOR

blockage of the motor (For example, a windy weather), which output torque is not enough. The situation can lead to a Exception happens when the motor is a step- motor and the when the slope error and the gear-diastema are all small option). No solar sensor is needed to track the sun, especially system clock is precisely set (Direct time from GPS is an will be within one percent of mrad (milliradian), when the DSP as the controller, the bias for the calculated sun position system. If the tracking system uses a PC or a high-performance it's necessary to include a solar sensor to make a closed loop position, because of its low process speed and low precision, MPU (micro- processor unit) is applied to calculate the sun's astronomic formula to locate the position of the sun. When a sensor system usually follows the equation based on the when it's cloudy. In the area of solar thermal generation, solar known that solar sensor will lose its functionality temporally both applied as two solar sensor to correct tracking bias. It is Dish type tracking controller and PV tracking controller can be

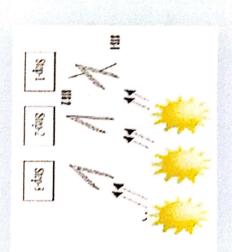
5. WORKING OF ASTS Basic Principle:-

being used with following names and functionality. executes predefined task in its software. These sensors are send their output to TDA 2822 ic. Then the microcontroller on solar panel. All sensors (each with different functionality) and their operation depends upon the intensity of light falling The basic functional blocks of this system are four sensors1,

automatic sun tracking is "V" shape exactly in the middle of the solar panel . The Sun Tracking Sensors (STS) These two sensors are mounted in

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sensors i.e. STS-1 and STS-2 are getting same amount of light. Step-1 shows that when the sun is in front of solar panel, both

the microcontroller (figure 3). Then the controller1 rotates motor, resulting the rotation of solar panel towards the sun amount of light. At this point the LDR i.e. STS-1 sends signal to gets repositioned with respect to sun and STS-1 obtains less In step-2, after some time as the earth rotates the solar pane

west. Night and Cloud Detection In a cloudy day light intensityis track the sun the whole day. If the panel stops rotation then less than a normal day sensor is same as that of NTFD except that it is mounted in the DTFD detects this type of fault. The mounting strategy of this conditions e.g. cloudy weather etc, the ASTS is supposed to working. Day Time Fault Detector (DTFD) except some special sensors i.e. STS-1 and STS-2, but as the fault arises, it starts lesser intense light (predefined) as compared to the middle panel so in normal conditions it does not work because it gets shown in figure-8, the NTFD is mounted in east of the solar detects whether the solar panel is ready for tracking or not. As morning it would not work. So at the next sunrise, this sensor if a general fault2 occurs during nighttime then the next Night Time Fault Detector (NTFD) in routine work of the system

7.1 ADVANTAGES:-

-

construction is simple This automatic solar tracker is easy to implement since its

consumption by the system itself. energy generated is around 25% to 30% with very less With the implementation the proposed system the additional

importance as an energy resource as fossil fuel prices fluctuate energy falling on it renewable energy is rapidly gaining The solar panel with the sun in order to extract maximum

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7.2 DISADVANTAGES:-

- This system cannot be used in rainy season.
- Initial cost is high.

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∞ .APPLICATIONS:-

real and very huge solar panel. This system software and hardware can be used to drive a

communication with the mechanical structure of solar panel. The computer and System Control Unit would have a wireless

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microcontrollers e.g. PIC 16F877A would be usedgure To make emergency control better more powerful 8: Flow

Chart showing automatic operation of ASTS

9. CONCLUSION:-





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DEPARTMENT OF PHYSICS

Mr.Roshan Kumar

SUPERVISION OF

UNDER

RAVI KUMAR NIRMALKAR





MASTER OF SCIENCE

OF

IN PHYSICS





Arduino Based Automated Vacuum Cleaner

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PROJECT REPORT

Govt .Nehru P.G. College Dongargarh

(C.G.)

Submitted to

In partial fulfillment of requirement for

The award of degree

4 | Page

Date: 9/5/24

Date:

(<u>konhan</u> Internal Examiner has been examined by the undersigned.

Degree

as a part of the examination for the award of Master of Science

in Physics in Govt. Nehru P.G.College, Dongargarh

(C.G.)

External Examiner

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BY

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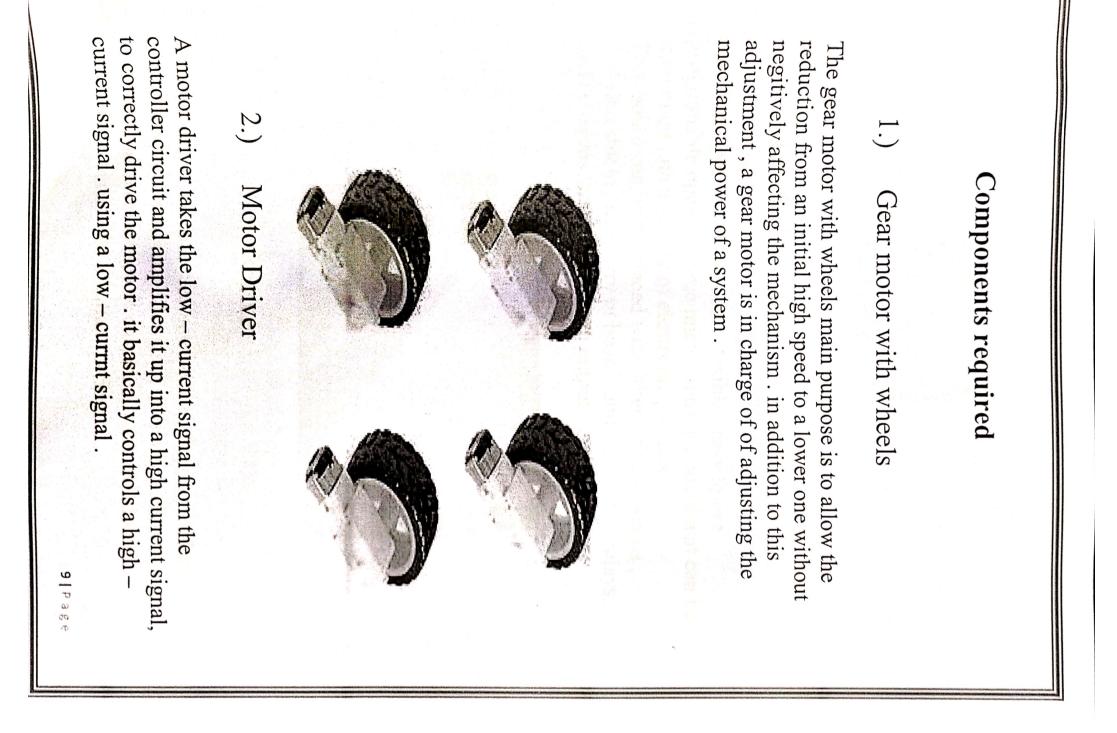
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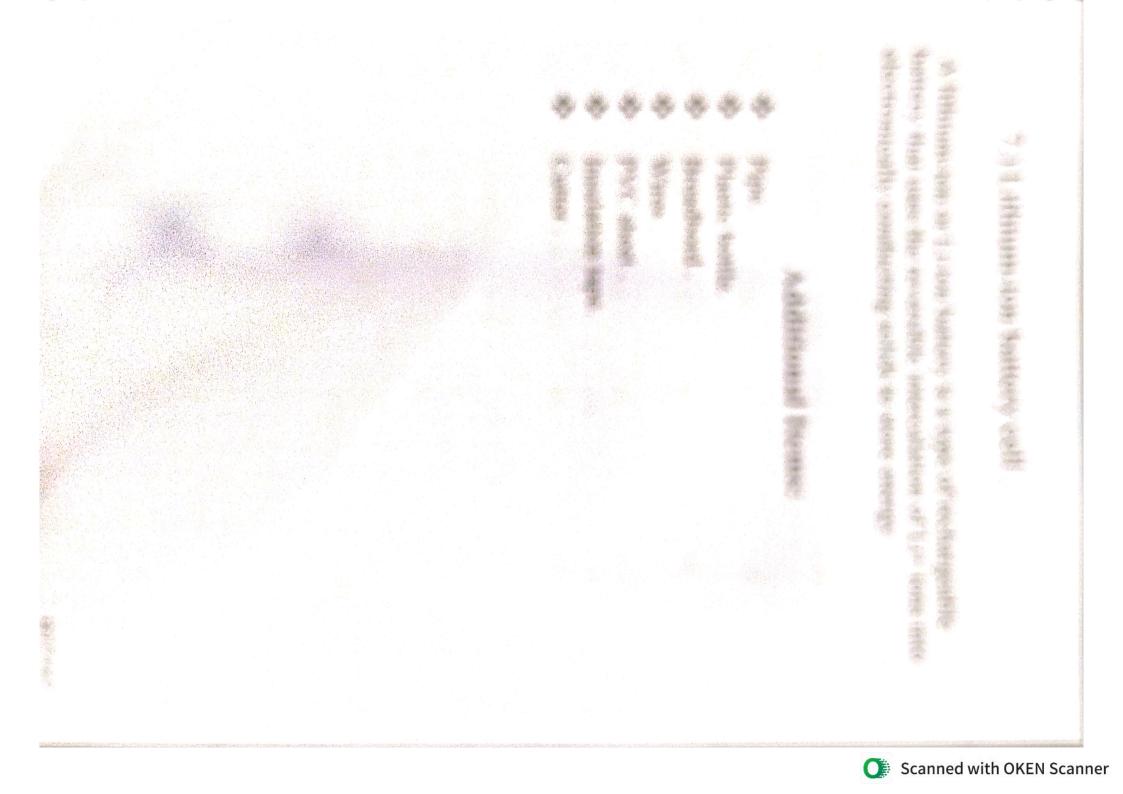
Arduino based automated vacuum cleaner

RAVI KUMAR NIRMALKAR

Submitted by

Roll No. 235070850014





Characteristics

- ÷ Navigation Technology & obstacle detection.
- ÷ Perceive the environment using the sensors
- ÷ computer programs Make plans using algorithms implemented in
- *** Filter system .
- 4.4 Mop.
- -----Cleaning history .
- ÷ Dustbin, Capacity.
- ÷ Maintanance.
- -Application integration.
- ÷ Suction power.
- $\dot{\phi}$ Cleaning modes.
- $\hat{\phi}_{i}$ Noise level.
- $\langle \cdot \rangle$ Shape cleaning.
- $\langle \cdot \rangle$ Cliff detection.

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the motor sheild As seen in the illustration, the Arduino UNO is mounted on

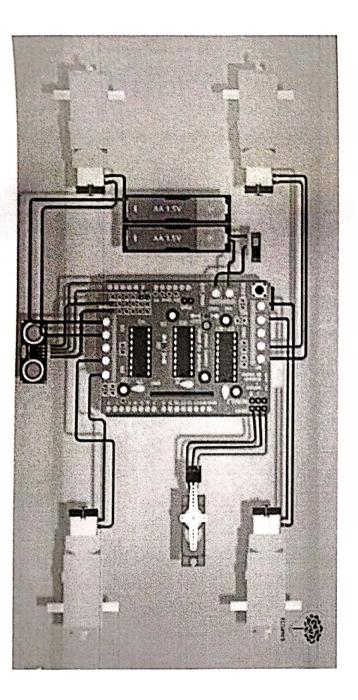
wheels that are wired to it. According to the code provided to UNO through the arduino IDE software, the motor shield contols all the motors and

allows it to avoid collisions that could harm it . it is the robot's This sensor identifies any obstructions in the robot's path and Over the servo motor, an ultra sonic sensor is mounted front that the servo motor is mounted on

motor and the ultrasonic sensor, respectively. The motor sheild's slots are occupied by the pins of the servo

side, power's the entire circuit. The robot can be turned on off whenever necessary using a switch The lithium ion battery, which is mounted on the robot's back

schematic as shown in figure All connections are done in according with the circuit







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DEPARTMENT OF PHYSICS

Mr.Roshan Kumar

SUPERVISION OF

UNDER

LALIT KUMAR

AT IT VIIM





MASTER OF SCIENCE

QF

IN PHYSICS



Arduino Based Automated Vacuum Cleaner

PROJECT REPORT

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Govt .Nehru P.G. College Dongargarh (C.G.) In partial fulfillment of requirement for

Submitted to

The award of degree

DECLARATION

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supervision of based on my work carried out during the course of my study under the work entitled "Arduino based automated vacuum cleaner " We undersigned solemnly declare that the report of the project S.

Mr. Roshan Kumar

(Assistant Professor, Dept of Physics)

has certificate in this college. my knowledge and believe, it does not contain any part of work which the outcome of the project work. We further declare that to the best of been submitted for the award of any on degree/ diploma/ We assert that the statement made and conclusions drawn are

ROLL NO. 235070850009

Name: LALIT KUMAR

219388

Abstract

surrounding enviroment is more arduous. In the current hectic schedule, cleaning houses and

to handle it. At present, there are vacuum cleaners which require humans

works whitout human intervention . thus, there is a dire need to implementvacuum cleaner which

risks to mankind. this is achieved by implementing cleaner, hazarous places can be cleaned which thereby reduce An autonomous system . implemented through this project . efficient method to clean the By using this desired are has vacuum been an

this project. vacuum robot prototype by using Arduino uno, motor shield the main objective of this project is to design and impement a Utrasonic sensor and motor with wheels to achive the goal of

the whole circuit is connected with 12 V battery. robot and obstacle. the ultrasonic sensor is used to measure the distance between

Vacuum robot will have several criterial that are user-friendly



Introduction

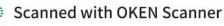
powered and controlled by an Arduino microcontroller. An Arduino-based vacuum cleaner is a cleaning device that is

the cleaner Sensors arduino , and other components that make board is programmed to control the motors up the vacuum

This power, and even navigate around obstacles. over the cleaning process, making it possible to program the Vacuum allows cleaner for a high degree of custimization to clean specific areas, adjust the and suction control

monitoring capabilities. or a home automation systems, to provide remote control and connected to other devices and systems, such as smartphone Adiditionally , an Arduino-based vacuum cleaner can be

tool, hobbyists interested in robotics and home automation. This makes the vacuum cleaner not only a practical cleaning but also a fun and educational project for makers and

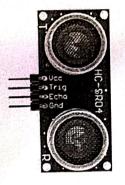




5.) Ultrasonic sensor

the ultrasonic transmitter and reciever. The model has two eyes like projects in the front which forms

pack. distance to an object. This Ultrasonic sensor module is a The HC-SR04 ultrasonic sensor uses sonar to determine the transmitter, a reciever, and a control circuit in one single



6.) 6v motor .

generators for geared transmissions and DIY they work fabulously as "mini" electrical mount or alligator clip leads to AA batteries power to light a small bulb! Pair with a motor wind pumps. Capable of producing enough Lightweight, durable and consistent in output,

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pecification

- ÷ the cleaner. Four power driven wheels is used for the movement of
- ÷, battery . The main power source of the cleaner is Lithium-ion-
- ÷ Arduino uno is the main controller unit of the cleaner .
- of the ultrasomic sensor . Servo motor is used for precise control and navigation
- ÷ Ultrasonic sensor is used for obstacle detection .

9	8			6	5	4		E Construction of the second s			2		No.
Width	Height	capability	Collection	Suction power	Current	Voltage	Material	Frame	angle	Movement at	Wheels	Totle mass	Specification
Mm	Mm		Gm	W	mAh	V		1			Degree	Kg	Unit
130	007	(approx)	250gm.	100	1,100	1 100	(plastic)	PVC			50	2.1	Value

Specifications / value

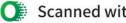


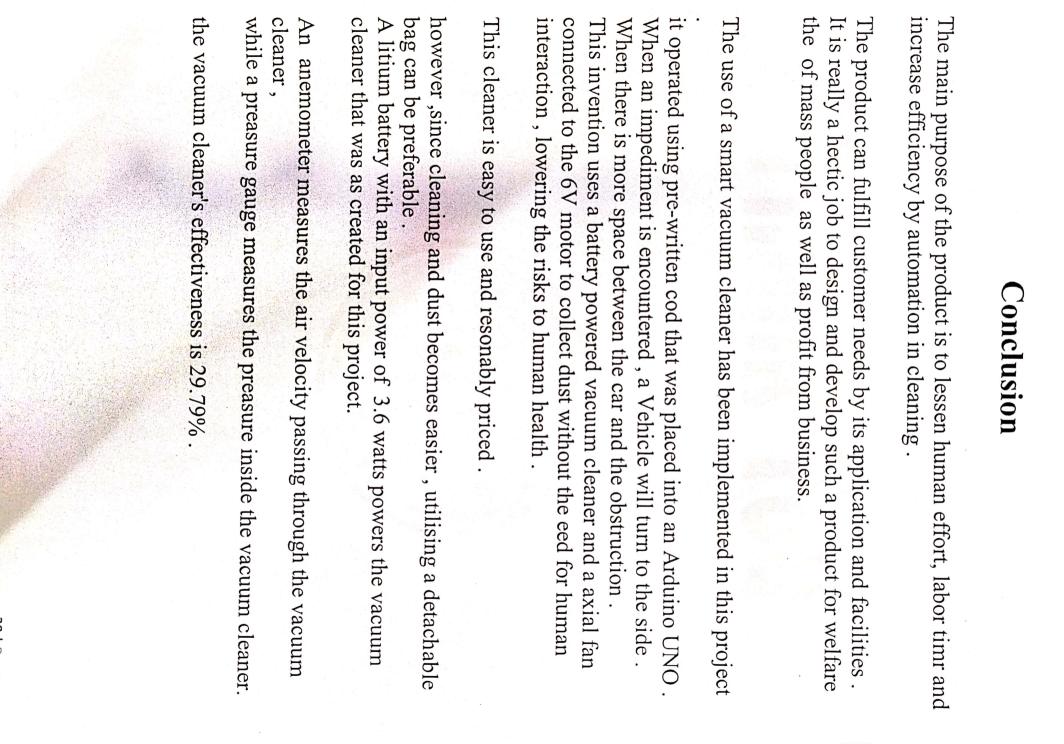
Advantages

- ✤ Save time and energy .
- ✤ Lesser noise .
- Cleaning by itself.
- ✤ Intelligent Navigation.
- \bullet no need to manual operation.
- Iightweight and compact.
- * Improved cleanliness and hygine.
- ✤ detects all levels of dirt.
- ✤ Affordable.
- ✤ Cordless design.
- Compact and easy to store
- Comfortable .
- Under furniture cleaning.

Disadvanteges

- ** Some models can be costly .
- ** Risk of getting stuck .
- ÷. Frequent dust bin changing required.
- ** long cleaning time
- ÷ Battery life
- •** Iffective on stairs and thick surfaces





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एम .कॉम चतुर्थ सेमेस्टर (प्रोजेक्ट वर्क)

वर्ष 2023 -24

क्रमांक	छात्रों के नाम	विषय
1 . `	आकाश वर्मा	शेयर बाज़ार (निवेश)
2.	आशा	महिला स्व - सहायता समूह (जैविक खाद का उत्पादन एवं विक्रय ग्राम - मेढ़ा के विशेष सन्दर्भ में)
3.	भावेश दास	मिनिरल वाटर प्लांट (रोजगार की संभावनाए)
4.	भूपेंद्र कुमार	प्रधान मंत्री मुद्रा ऋण योजना (केनरा बैंक डोंगरगढ़ के विशेष सन्दर्भ में)
5. _.	चित्राणी ^{————}	केंद्र सरकार द्वारा संचालित "हर घर नल जल योजना" ग्राम - सेंदरी के विशेष सन्दर्भ में)
6.	गंगीता	स्वरोजगार हेतु दुग्ध उत्पादन उद्योग की भूमिका (एक अध्ययन महर्षि सद्गुरु सदाफल देव आदर्श गौ सेवा केंद्र ग्राम - सेंदरी)
7.	गुनगुन अग्रवाल	लघु उद्योग के आधार पर शृंगार सदन का अध्ययन व विश्लेषण (विकासखंड डोंगरगढ़ विशेष सन्दर्भ में)
8.	हेमा पटेल	राज्य सरकार द्वारा संचालित "मितानिन कार्यक्रम क्रियान्वयन एवं विश्लेषण "(डोंगरगढ़ ब्लाक के अंतर्गत ग्रामीण क्षेत्रों के विशेष सन्दर्भ में)
9.	जितेन्द्र कुमार	एबीस फैक्ट्री राजनांदगाँव के द्वारा श्रमिकों को प्रदाय की जाने वाली योजनाओं का अध्ययन
10.	कोमल साहू	स्व - रोजगार के क्षेत्र में बेकरी उद्द्योग की भूमिका (विकासखंड डोंगरगढ़ विशेष सन्दर्भ में)
11.	लोकेन्द्र चंद्रवंशी	सेवा सहकारी समिति बेलगाँव के कार्यों का विश्लेषण
12. 🗇	महेश्वर	जिला सहकारी केन्द्रीय बैंक द्वारा प्रदान की जाने वाली सुरक्षित ऋण का अध्ययन (विशेष सन्दर्भ- एल. बी. नगर)
13.	नीलम ^{-ेर} े	भारत सरकार द्वारा संचालित ग्रामीण स्व - रोजगार के संबंध पर शोध

14.	ओमेश	छत्तीसगढ़ सरकार द्वारा चलाई जा रही मुख्यमंत्री पेंशन योजना की समीक्षा)
15.	ओमकुमारी वर्मा	केंद्र सरकार द्वारा संचालित प्रधान मंत्री उज्ज्वला योजना का अध्ययन (विशेष सन्दर्भ- एल. बी. नगर)
16.	राहुल वर्मा	यूनी वेब स्लीपर फैक्ट्री का अध्ययन (विकासखंड डोंगरगढ़ विशेष सन्दर्भ में)
17.	राजदीप कौर भाटिया	" A study of Process and Problems of Digital Marketing (A special reference in Dongargarh Block)
18.	रीमन	महिला आत्मरक्षा
19.	रूपलें कौर भाटिया	" Prime Minister Employment Generation Program"
20.	संतोष कुमारे.	केंद्र सरकार द्वारा संजालित भारत नेट परियोजना का अध्ययन
21.	टाकेश्वर साव	मुख्यमंत्री कौशल विकास योजना का अध्ययन जिला- राजनांदगाँव के विशेष सज्दर्भ में)
22.	विजय अमरदास	सीमेंट उत्पादन एवं विकास का परिचय (जिला बलोदा बाज़ार के विशेष सन्दर्भ में)
23.	यामिनी	आगनबाड़ी के कार्य एवं योजनाओं का अध्ययन (विशेष सन्दर्भ- ग्राम पिनकापार जिला- राजनांदगाँव)

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दिस्तानाव्यक्ष सात्राज्य विस्तानाव्यक्ष सात्राज्य कार्यकाय नेहरू रनातकोत्तर महाविद्यालय जोगरगढ (छ.ग.)

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परियोजना रिपोर्ट

पर

परियोना रिपोर्ट का विषय - जिला सहकारी केन्द्रिय बैंक द्वारा प्रदान कि जाने वाली सुरक्षित ऋण का अध्ययन



एम.कॉम. चतुर्थ सेमेस्टर " वाणिज्य संकाय "

।। जमाकर्ता ।।

महेश्वर एम.कॉम. चतुर्थ सेमेस्टर रोल न.-**"वाणिज्य विभाग"**

शासकीय नेहरू पी.जी. महाविद्यालय डोगरगढ़ छ.ग.



॥ मार्गदर्शक ॥ जी.के. यादव



GOVERNMENT NEHRU PG COLLEGE, DONGARGARH

शासकीय नेहरू स्नातकोत्तर महाविद्यालय, डोंगरगढ जिला – राजनांदगांव (छ.ग.) Affiliated to memonand Yadav Vishwavidyalaya, Durg University Accredited by NAAC with Grade "B+" (CGPA – 2.61)

महिला स्वंग सहायतासमूह के स्वरोजगार हेतू जैविक जाद का उत्पादन एवं विक्रय का अध्ययन

(क्रि. केंग्र. डोंगरगढ़ विकासखण्ड जिला राजनांदगांव के संदर्भमें) मास्टरऑफकामर्स की उपाधि हेतु प्रस्तुत एम. कांम. अंतिम (चतुर्थ सेमेस्टर)



सत्र :- 2023-24

परियोजना प्रतिवेदन

निर्देशक

शोधार्थी

बेद प्रकाश साहु प्राध्यापक वणिज्य विभाग

आशा

एम. कॉम. चतुर्थसेमेस्टर

वाणिज्य विभाग

शासकीय नेहरू स्नातकोत्तरमहाविघालय डोंगरगढ़ (छ.ग.) संबंद्धता–हेमचंद यादवविश्वविघालय दुर्ग (छ.ग.)

A STUDY OF PROCESS AND PROBLEMS OF DIGITAL MARKETING

(A Special Reference in Dongargarh Block)

FOR THE DEGREE OF MASTER OF COMMERCE

(4TH SEMESTER)



PROJECT REPORT

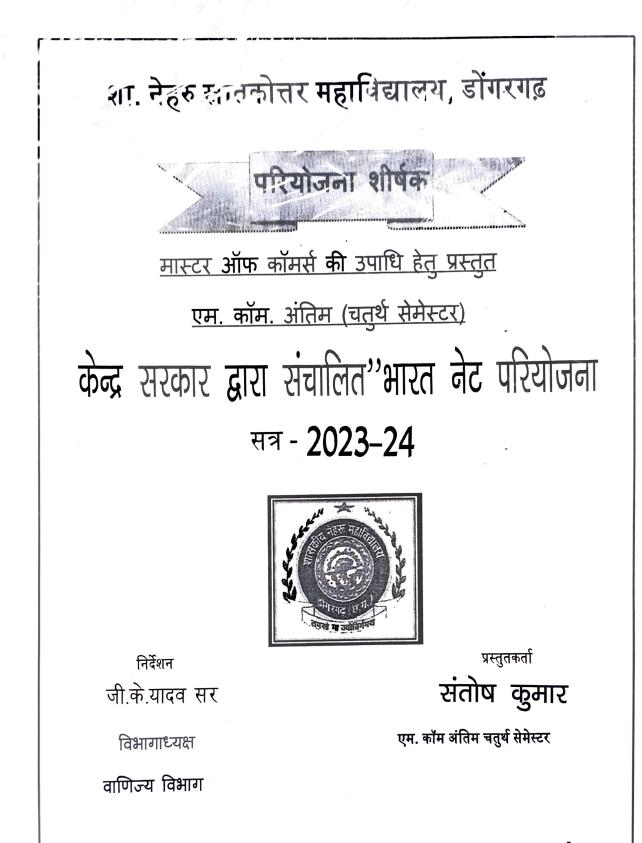
YEAR - 2023-24

GUIDED BY MRS. JYOTI SAHU COMMERCE DEPARTMENT SUBMITTED BY

RAJDEEP KAUR BHATIA M.COM 4TH SEMESTER

DEPARTMENT OF COMMERCE

GOVT. PT.JAWAHARLAL NEHRU PG COLLEGE DONGARGARH (CG) AFFILITED BY HEMCHAND YADAV DURG UNIVERSITY

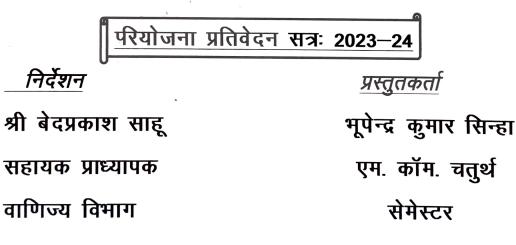


सम्बद्धताः– हेमचंद यादव विश्वविद्यालय दुर्ग (छ.ग.)

प्रधानमंत्री मुद्रा लोन योजना (केनरा बैंक डोगरगढ़) के संदर्भ में

मास्टर ऑफ कामर्स की उपाधि हेतु प्रस्तुत एम. कॉम. अंतिम (चतुर्थ सेमेस्टर)





शासकीय नेहरू स्नातकोत्तर महाविद्यालय डोगरगढ़ संबद्धता– हेमचंद यादव विश्वविद्यालय दुर्ग (छ.ग.)

A PROJECT REPORT

ON

" PRIME MINISTER EMPLOYMENT GENERATION PROGRAMME " SUBMITTED IN PARTIAL FULFILLMENT OF CURRICULUM OF POST GRADUATE DEGREE IN MASTER OF COMMERCE.

GOVT.PT JAWAHAR LAL NEHRU COLLEGE, DONGARGARH



PROJECT GUIDED BY.

JYOTI SAHU

SUBMITTED BY

ROOPAL KAUR BHATIA

M.COM (4th sem.)

AFFILIATED To: -

DURG UNIVERSITY, DURG (C.G).



Date Page No.1 Roll No. रलोबल वामिरा 05120 यह बात तो हम खाब जानते हे फि पृश्ती का बढ़ता तायमान जानवर, प्रत्येक मनुष्य अमेर तन्य जीवन त्यानी वि प्रथ्वी के हर जीवित जीव को प्रमावित करता है। कहीं को छिरायस पिद्यल रहे हैं तो छहीं पानी मही है कही बाह की समस्या है तो की कहीं खुरते की समस्या है स्रोर रेसा नहीं है कि हम अब उरफी अंजान हैं। समय २हते हम लोग अच्चेत नहीं हुए तो एफेर इष्मके धातफ परिणाम हम सबकी इनेजने पड़ेंगें इसालिए ज्लोबल वार्मिंग के करनों Reasons of Global Wasning के बारे में धानना अत्यत भावश्यक हो गया है। त्रिमा शहर हे Global - Warning जिससे लंगभग हर कोई धरिष्यित है। लेकिन इसका

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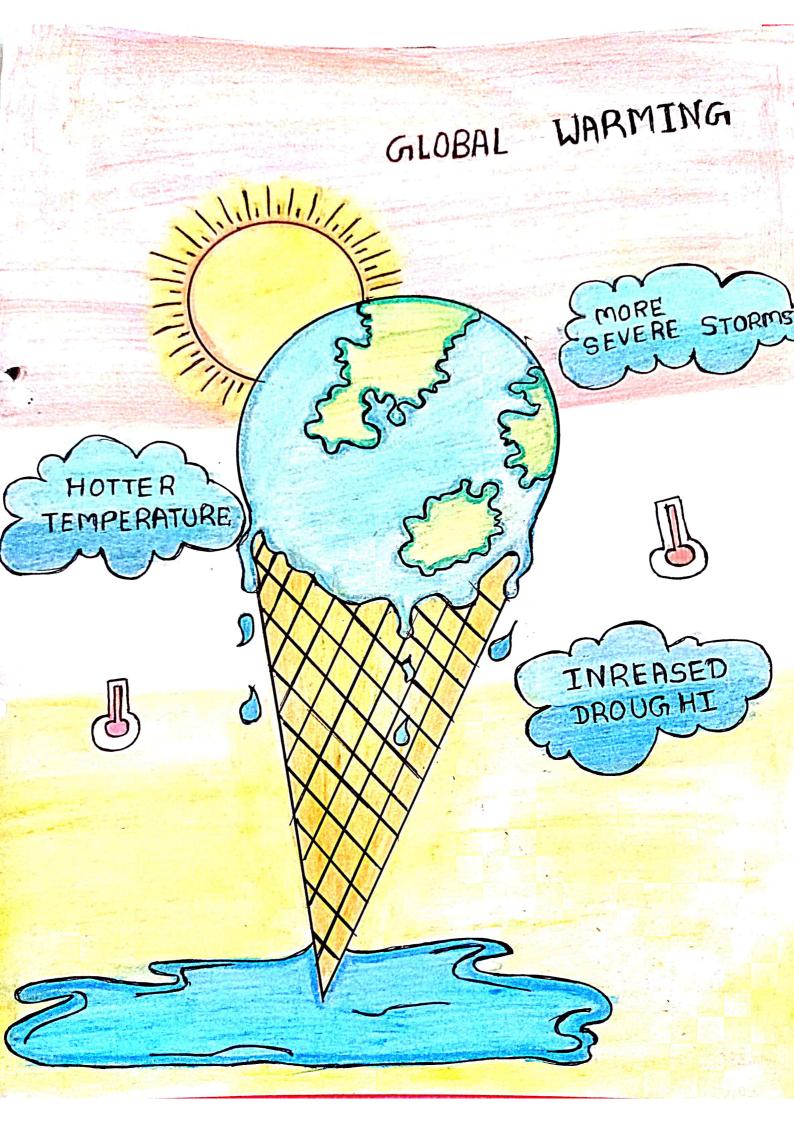
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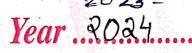
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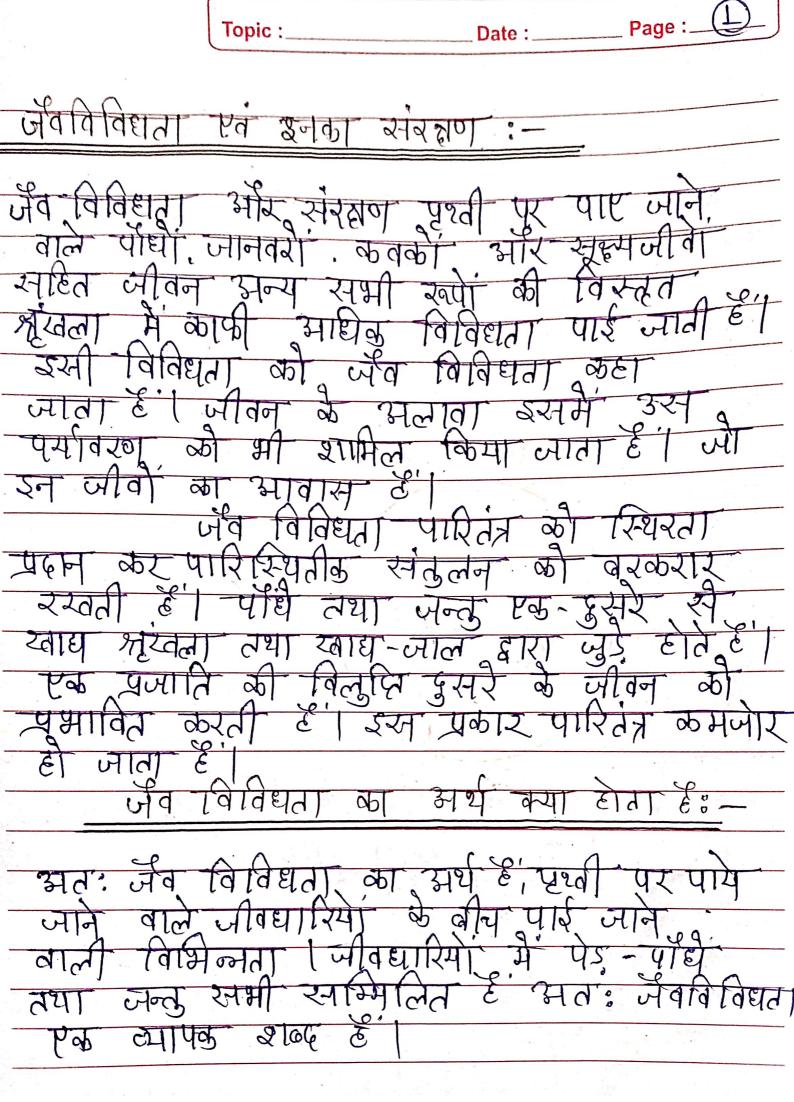
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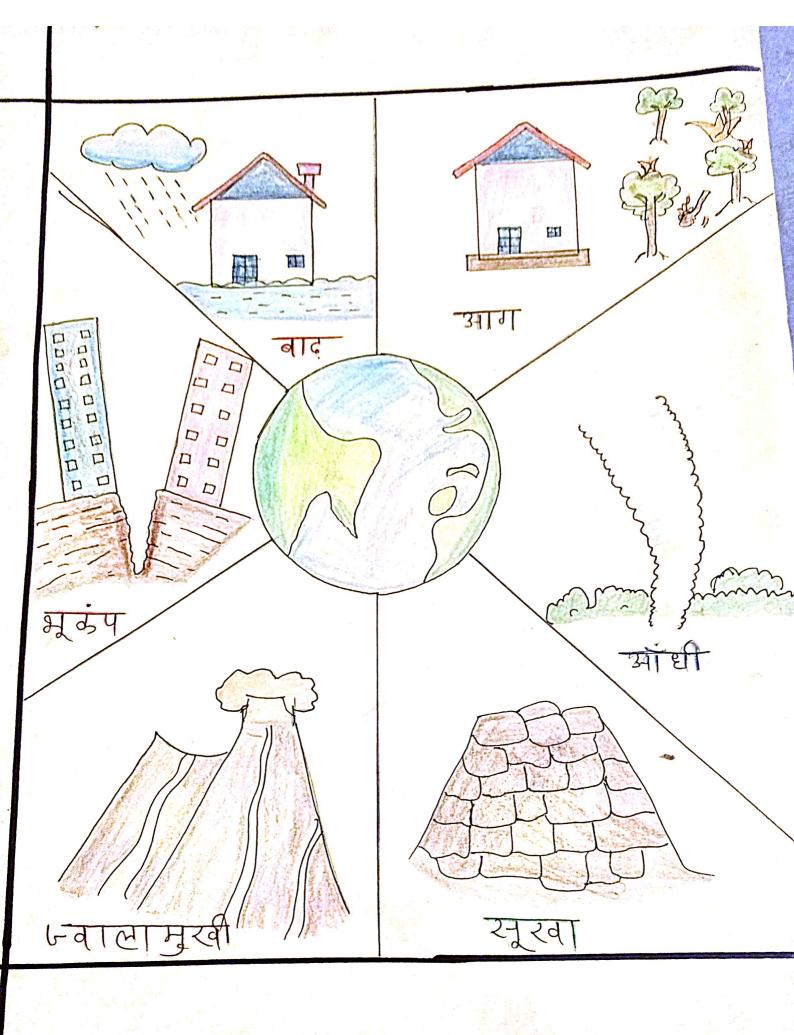


Guided by

Submitted by

Topic :





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Page No. Date पदा प्रबद्यन में अ तेयाूरी 311441 2-प्रस्तावनाः- आपदा प्रबंधन विके छिंद तें यारी जमूय छहि किया देने' आपदा aud 4 वडी विफलताओं कि परिणामों दिनीखने की प्रक्रिया शामिल रियमे किसी आपदा के कारण के कर्ष वाले मानवीय भौगतेक. पर्यावर्णीय प्रभावी रने 311210 1-1927 3-115 RITH G S रनमुबन राष्ट्र के अनुसार आपका एक ट्रमवूष्टान को संदार्शन हरती हैं जो हिसी समुदास सा समाप के जामकाज में वाधा उत्पन्न कर ती हैं। झापदा से जनसमुदाय संपाद्ती अर्थत्यवरूधा धोर पर्यावरण , पर दयापक नकारात्मक प्रभाव पड़ता है। प्रभाव SIMURE SPIRES के पकार अगपदा अगपदाष्ट्रं विभिन्न रूप ले ZABAT धोर रनमुदायों को गम्भी रेगिरूपन दे वा कर रनकती है, जिल्लेस टयब तियों संप GIE ट्यवसायों भोर पर्यावरण पर नढां रात्मठ प्रमाव हो रनकते हैं। वे छाय: किसी जनसमुदाय की बचाव एव रनामना करने

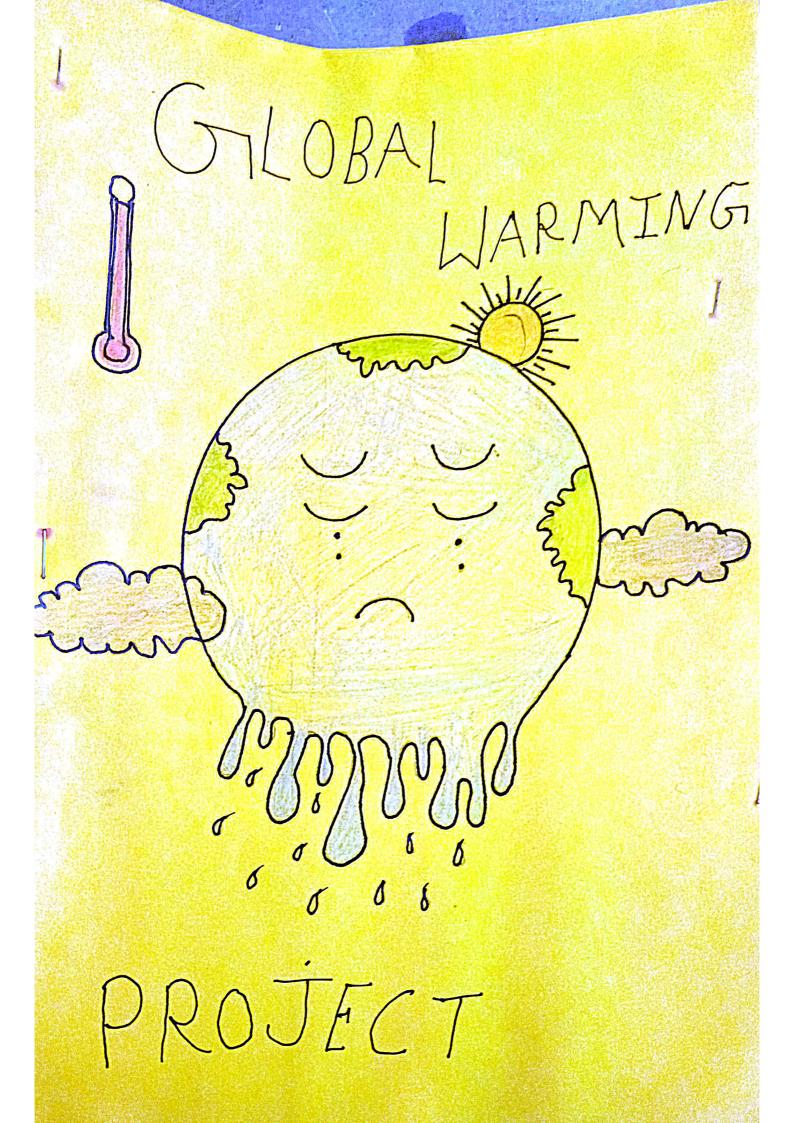
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14 Page No. Date -1 14 ** प्रस्तावला 34 Had Jalaci altar ul 052 MYHIM 34 HNNQ 9 ** ver 24-1 Han 27-14 UTT 3.8 PEAT D 3110 ** 27291 GC JTT 076 D) EIZATZ Harr ** 007 44 Y वहत्म दुखा दिखेगा ** * * Janar . JT OT का अन्ते - Segt * al * Q126 arey of 2Ja opizor zita * Ta dean -JEGT alaHIM DE DE DE * ater 1 total . 100 के संसत TFT HIST * आव्या पर गः कारेनराइट קצור * THOMA PORCH ดการส SIGNIO, FEHROOST STIP न्याद्वी कि TUERT 2772 \rightarrow A lac भार के जल - Ontail LISIN كروع NUI YY TTIA N 60 E ATHON रमका ले 3.77 वार्तिग द्विया าย เมร 4 John of Bi NGT 212 2772-211 ATA otel *<i>पत* समय

PROJECT On 4-5070040 लाम - जन्तल साह (2मेश माह) <u>क्सा - B.A - I</u> aur - arkinzoi Year 2023-24 Submitted by Guided by

* * * * * (\$\$ 蒙 蒜 12 禁 芬 3% 17 Date : युव द्यापदा ध्रवदाल , श्रव्य लेख, -राकृतात , श्र्रा रुरतहान । Page No. 01 22 3% <u>बापदा का ताल्परी</u> आपदा उस विषय स्थित से है। जो मानन की 於 जी मालव के प्रापित तथा समाधिक कारको की ** प्रमावित करती हैं तथा आमाल्य जीवनन्त्रय मे ** द्राहिक व्यवशान डालती हैं। मनुख्य के छिरााठलापी की तरातिरठा का रूप विगड़ता जा रहा। इसमे ** पर्धार्थतरुवा असंतुबन उत्पत्न हो रहा हा जिसमे 24 -छानेक आपदे का प्रार्डमाव है। ** - 3 मिंड राम्य कि दी मागार -** <u>। प्राकृतिक कापदा</u> – प्राकृतिक संतुब्ध में व्यवद्यान में कालि से जो बाधारे उत्पन्न होती है। उसे प्राकृतिक कापदा कहा जाता है। हो आपदारी वायुमंदर कार्यना कर कर्ता कर कर कर कर कर कर कर ज्यहनी के क्षतिर से उत्पन्न हो सकती है। ाउँ ामार इग कि रत्तर क्रास्तित्व रत्तरे में पड़ जामा है। # प्राकृतिक आपहा के प्रकार -0, म्राक्य एव झाल्याला - प्रश्ती की सतट की हिल्मे ÷ को कहने हा राह घुरती के स्पालमण्डल में अउमी के जान्द्रत के जान के जार्ग अन्तन होने ताही मुक्रमीय तरनागी के कारन से होता है। भूकम्प बहुत हिंगात्मक हो सकते है। सार फुध् ही क्यां में लोगों के गिराकर चोट पहुंचाने से लेकर पुरे जगर को ध्वरत कर स्कर्म ठी इसमें क्षमता होती है। भूकम का मापक भूकम्प मापी यंग रो किया जाता है। जिसे म सीरमोग्राष कहा जाता है।

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On 45070087 गीताजली कहम - राष न्थम जः 0 पर्याक्रा अयोजन -042

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DATE ****** BJECT. वमि 9 94 QM उपार 9150 C 2x20 त्रस्तावन D 10100 वामग मरा ग्लावल 9 960 वामुग वाम्या G ल्पूत्रभाव ग्लाबल y ग्ल बिल 9 19150 dung उपसंहार 6 H 6P रलावल हमार JD য NZP, लए 21 dri AHS E QG लगाता 2 धरता argieo G 9 kg मनुएर 55 EA स्मम्स्य स G 90 5 धरत 392 पर REG पाल G ga AG h 9 लित पृ 9 SH समस्य 6 46 8 व्यक 64 9× 46 3E 5 41 0 REI ચદ लाभन 19/96 15H P 19 qç QALE g बजास 9246 नरतर 96 रु स 229 स्तट तापमान PAda 91 6 तापमान स बहुत 261 915 6 0 गेरी θ 950 0 5 dO d 0 Solde SIS 1 62 स TEN 144122 1419 31 9 पृश्व 6160 स्पतह RI g la G 75 1 ALYHIAqli Qee 91 10/10C eld 929 G or y HSM 900 515 4815

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Year 2023 - 24

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Submitted by Mansi Bais

Date: - 8 TOPIC **Fopic**: प्रबद्धन भूकप -yohald ,016 12175 2-190101 9<u>9</u> -9 2211 शटना 31-21010 99 91120 EITO का तबार 20 31461 21241-1 Add 3112 विनाश रन 29 EITE 1 EI 311461 8412 ONTA Ella O allor 344 3414G1 00 100 3412 Utel a 123 212 420 त्रकार मानासक 00 झेश आधिक प्रक्षावित 3-11 , राजनीतिक शार ति का सम्राजित आपदा का व्यवाधन सामाणिक रेमारेक रियात रेगमान्यतः अभवा का व्यवाधन जिञ्चात्मारवत त्रभाव पड़ता यह समान्य दानक प्रतिन कर प्र तरह स जाधित कर दता Teacher's Signature

PROJECT

नाम उपासना वर्मा बी र आमाग दो आसः नेहरू जनातकोत्तर महाविधालय डोंगरगढ रोल न – 45071019 विषय – पर्यावरण

On

Guided By

Submitted By

TOPIC DATE 04/01/2024 PAGE 01 र्वाश्वेक तापमान निर्यंत्रण का अनिवार्य छोर रूळीकत हृषितकोण अनेक पहलुओं को सम-हित करता है। नीचे आपको द्वस महत्वप्रण मुद्दे की जहिलता और गॅमीरता को उजागर करने के लिए कारगी और समाधानी का विस्तत सैक्हम मिलेगा। वेश्विक तापमान के कारण जीवाष्टम ईद्यन ह- जीवाय्म ईद्यन की -चालना ्रम ज्यादातर कार्बन -डाइअंक्सिइड मुक्त होता है, जिससे कीनहाउस प्रभाव को बढावा किलता है। 2) तनस्पति सेंदार ३- तनस्पतियों को खेली, काटने से पहले जैंगला को आफ करने व्हे पृथ्वी की क्षमता को कार्बन अोधन करने के लिये खढ़ावा डाइयाक्साइड को होता है, जिससे करीनहाउस प्रमाव में चडि होती है। 3) और्योगिकीकरण 8- ओंयोगिक क्रियाकलाप) की तेज विकास की कीनहाउस गैसे बद जाती है, जिससे वैश्विक तापमान में



- *********** 0 2049211 नाम - नमता पिता - आमोल सिंग (bg) - बीः ?: प्रथम वर्ष लिवय - पर्यावरण रोल न. - 45070186 2-17/ - 2023 - 24

OI IBJECT ****** र्ने संरहाग इसको सत रण्त 20 छी Ъ 21.0 91 1917 आव 17-1 D 21 -5 51 पाय 6 जातया 5 21 0 क 31 9 cЬ 2-21 Я 34 Я 510 CI a 8 छहत é 00 45 प्रदित З Ч eP. 1-1-1 E H 31 3-3 C 0 2 3 पाय ह्यकत anism त 0 agy 510 10 ch, হাতান geal 2HG UY <u>क्ति</u> 210 <u>ি</u> 'तय Q भूग 57 3 2 800 H 60 लय आहार CP 51 2 20000 न्दीाट या O की ञ 8 ६ प U तया 5 9 siet पाध जात C वहाला Neusi विद्या C dep OF NSO Č 004-7 19 0, g पर 50 -dp वल्र a Q य 3 Elc Oh दया 20 J3D 5 S 0 31 010 ch 21 1-1 ehe E ~ ۴ 2 3 वहात। q C 0 Heno 10 0 2 স 0 0 जहाता 4 वा 2-2-252 3 * * * * *

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वेविद्यता रुवे इसका संरक्षण Ricdive.usity विभिन्न 2-1 आवास) पाद्य त जन्म जातासा ज विभिन्न प्रकार के जीवीं की उपस्थिति रेल तिभिन्त प्रकार उपस्थिति co i जैव - मतिबिद्यता कहते सलग- सलग तरह के सावास स्टबी पर उन आवासों में तिबिाण्ट अनुभूलन युक्त प्तीत (Organium) पार्ट्य व्यक्ति सम्भव लह्ये हे । आगको ज्या हे जिलको नाठानी आगको ज्यान सथ्दी पर लग्मग उलाख लोरल (भूग) ्यातिश, २४००० महलिशां की जातियां, २०००० से अधिक सीटिया (क्रिटीयेक) की पातियों पायी पाती भाषिपादती 2 C र्जेव विविद्यता (Biodivensity) र्जेव विविद्यता बाद्ध एउवर्ड विल्सन् ने जेविक लंगडन के उत्रोक स्तर पर गांधी जाने वाली विविधता के लिये दिया, जेव विविधता की कई उकार के लिये दिया, र्त्तः -ये विभाषित किया जा अळता ह j जान्त्वै क्रिक विविद्यता (trenetic aliversity) tijuinia anderni (Species aliversity) in upperender a later (Ecological diversity **

PROJECT

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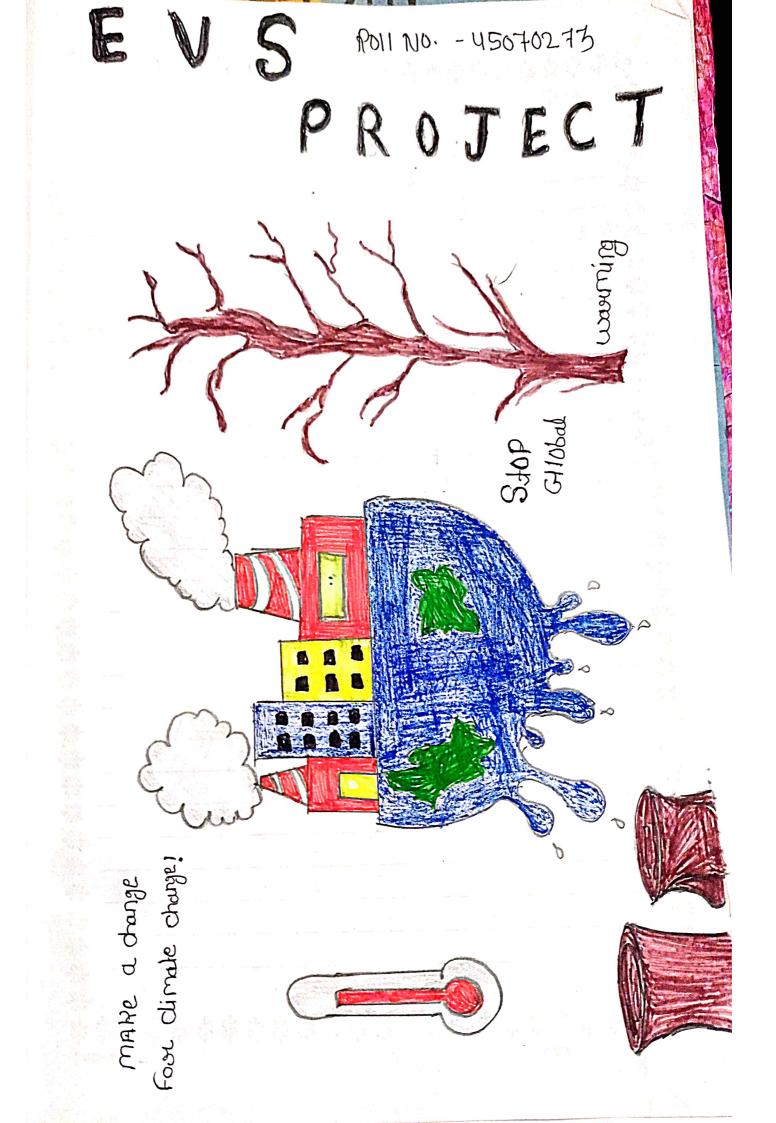
Uear 23-2024 -

Guided By

Submitted By

Asisha la Pasiver

Page : -Dale: 8-2 - 24 Topic . जैव- विविधा रुवं सेरहाठा आप भू आकृतिद पाक थाओं विद्यावकर अपहाय और विष्मून्त जलतायती होत्रों में अपहाय आहि के तिषय में पहले हो पढ़ युके हैं। यह आपको समरुग नहीं है, तो सांहादत सार के लिए FRAI 25 314612 Giarre (Weathering man वनस्पति विविधता का आधार तितिद्यता छा आधार माना गया है। सीर् ऊर्ज और यल ही अपझय मे विविधता और इसके परिणामस्वरूप उत्पन्न जैव- विविधता का मुरव्य छारठा है। इसमें छोई आश्चय नहीं डिवेहीत्र, लहाँ अजीव जल हो उपलब्धता आधिक है, वहीं जैव- विविधा। 211 C21143 2-12 42 E आज जो जॅव-विविधता हम देरवते हैं, वह 2.5 से 3.5 अरत वर्षे के विकास का धरिणाम है। मानत जीवन के पारंघ्य होने को पहले, ध्वती पर जैन-विविद्यता किसी आ अन्य जाल से आधेक धी भानव के आने से जैव-विविद्यता में तेजी से उमी अभि लगी, क्योंकि किसा २०७ या अन्य प्रधातियों की संख्या 20 लाखन्मे 10 फरोड तफ हँ लेकिन रे करोड़ ही इसका सही अनुमान हा नयों प्रात्मी भी रवोय लगातार वारी है। (२९ अनुमान के अनुसार पहिला अमेरिका की ताले पानी को लगमग 40 प्रतिरात



GOVT. NEHRU PGI COLLEGIE DONGARGARA ञाम - तन्तुजा वमी पिता का नाम - आ सम्पत वर्म - BAI - कह्या - रामावीला वताम MO.NU. - 6267698604

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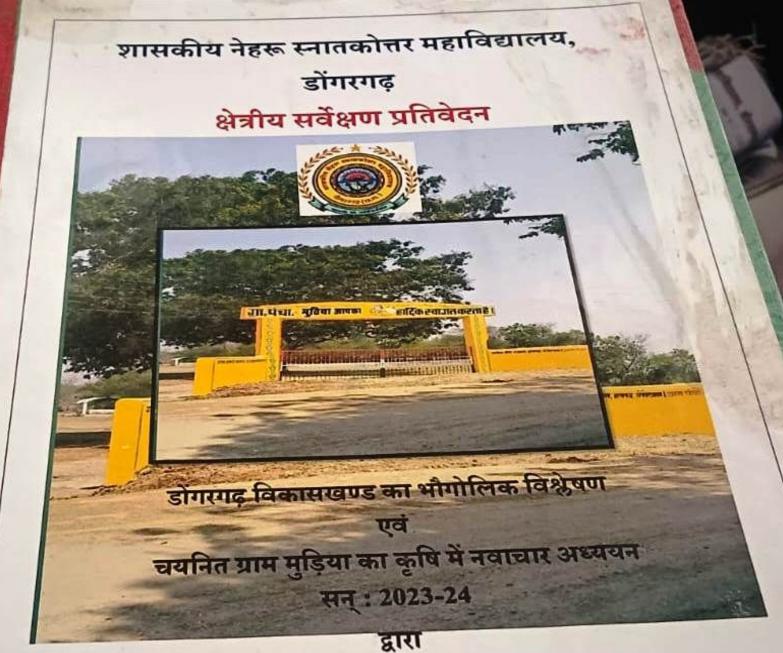
Date 07 162 124 Page - प्रस्तावना -वर्तमाल समय में विष्ठव की सवसे वड़ी समाए-था नलोबल वामिंग की है। नलोबल तामिंग के छारण धर्वन की स्वतह छा ताप्रभान तिरन्तर तद एहा है। तापमाज (11पमान ग्वाहन्तर बढ़ एहा है। तापमाज जेने घारिद के फार्ह्या प्रुरि विश्वत में उत्पन्न ही एही है अंधुक्त ए। राष्ट्र -र्पाय की व्यक्त इंग्लॉफिक्ल आगनाजी व्यक्त अगई एवल्ड मेदालॉफिक्ल आगनाजी का अध्यसन करने के बाद रीवताजी जारी की की गर्म होती धरती एक ख़तरनाक अंजम की ओर वर्ष्ट्र ही ही न्योन हाउल्य नेसि छा उत्सर्जन इस खतरे छी ओंच बढ़ा भे 139 उलीवल वार्मिंग कथा है'-विश्व में बिगड़ते हुपि पर्शवरुग संतलन प्र्वं प्रद्वार के जाएगा बुखी के तापमान ने तिरंतर हरिद ही रही ही जी कि जिवधारियों, प्रक्षी, ध्वं मानत आफ्रि के लिए खतरा बन गणी है, छले ही ACE



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DATE **** SUBJECT GIG भुजम्प Θ ٩ पतन्धनः उनापदा भूव-२०७न ज्जीर 290011 **अगप**का पुल-धन EARI STIR O 2700 साल O041461 62441 \bigcirc 411 E111 HILLO 905 6/07 0 HI 91 SEATTLE () 2410154 OHIMA उनापद्राउना लहा OSFIPT ULLI ONI 20015 [CI [Heat DARD 3 0110 E **UST** OX ell आमारिया arl त्रमामना 01111 69 तिन्छन 57t1: 2 641461371 Ð ∞ 691 2/11/2 20 ध्यान आपदा रुप्त 壮 जाग्राम 5 919 रिषि 010197201 17 360 12 20 EIMIJO 612 79011 24 QGA (D)91 लहा 1000 3 0181 तर0मरा HY -02/2 CD C प्र (n 12 U 17/10 [3 シイ 111 136 EH 72341 0 C2108121 187 -0 मियभू 90 4(1)1011 a)UR 91 (T Q) 1121051 021 NON 0 6 09 GR 17/01146 E 1221 011 218 0710110 64184 Jark 0111 (D)UTIO DIVI CS77 ICO SIEK 16101 3 1042 Ma) 4101 Moll 1822



गीतेश नेताम एम. ए. चतुर्थ सेमेस्टर भूगोल अनुक्रमांक : 235070530004, नामांकन क्र. HU/507/19001088

क्षेत्रीय सर्वेक्षण प्रतिवेदन

डोंगरगढ़ विकासखण्ड का भौगोलिक विश्लेषण एवं कृषि में नवाचार (चयनित ग्राम रामाटोला का प्रतीक अध्ययन) ICHRU

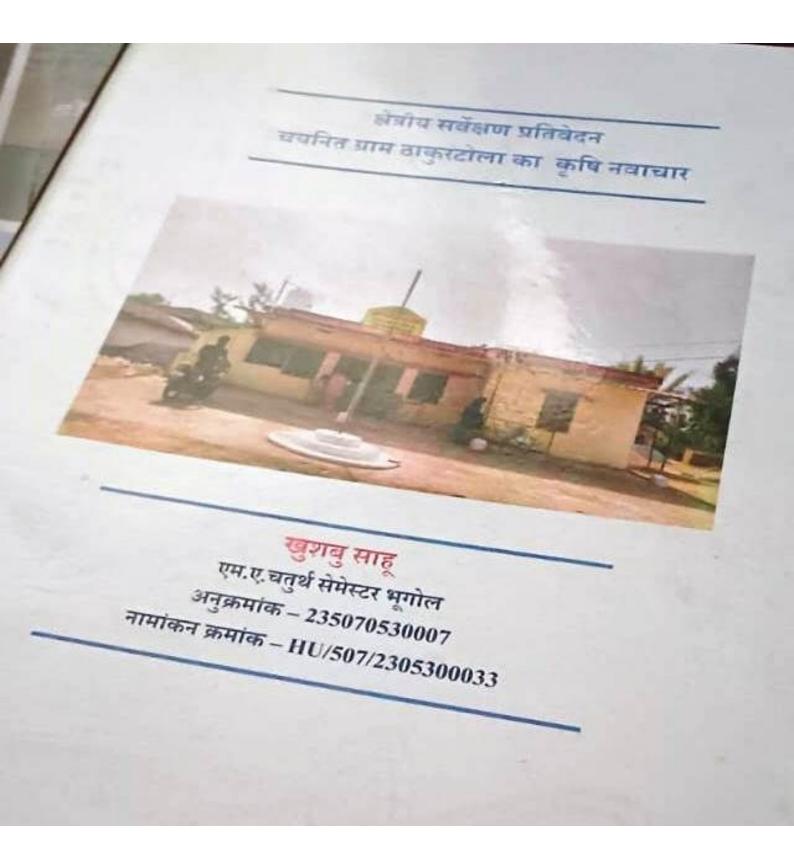
सत्र 2023-2024

टोमेश्वरी वर्मा

एम॰ए॰ चतुर्थ सेमेस्टर भूगोल अनुक्रमांक : 235070530023 नामांकन नं. - HU/507/230530010

प्राचार्या

शासकीय नेहरू स्नातकोत्तर महाविद्यालय डोंगरगढ़





-क्षेत्रीय सर्वेक्षण प्रतिवेदन-डोंगरगढ़ विकासखण्ड का भौगोलिक विश्लेषण एवं चयनित ग्राम धनडोंगरी का कृषि में नवाचार का अध्ययन सत्र - 2023-24 द्वारा कु.चेतन कंवर (एम.ए.) चतुर्थ सेमेस्टर (भूगोल) अनुक्रमांक-235070530001 नामांकन नं.-HU/507/230530001



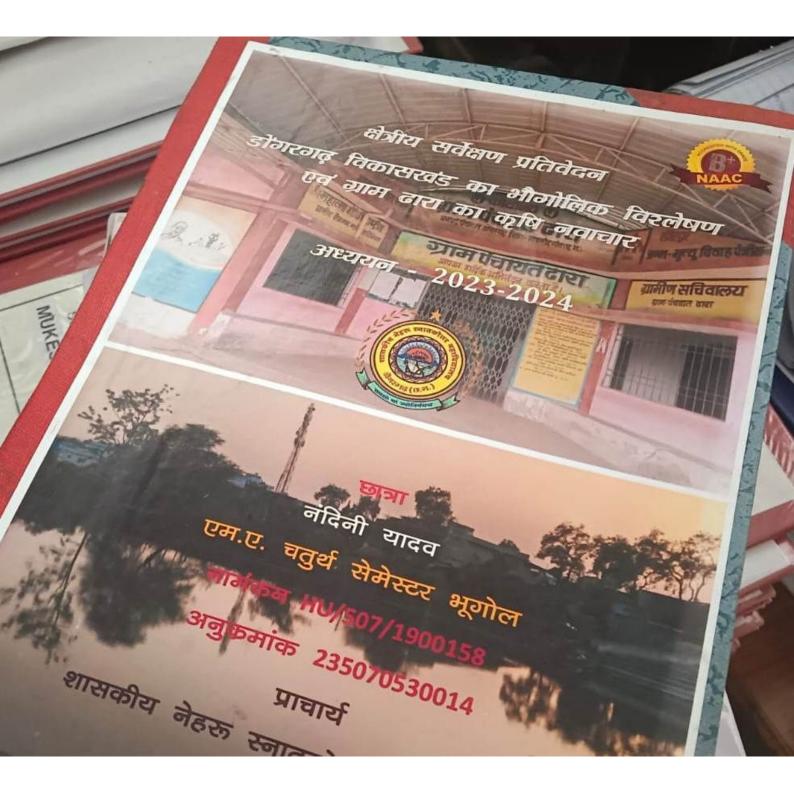
क्षेत्रीय सर्वेक्षण प्रतिवेदन

डोंगरगढ़ विकासखण्ड का भौगोलिक विशलेषन एवं कृषि में नुवाचार, चयनित ग्राम– देवकट्टा का प्रतीक अध्ययन सत्र 2023–24



द्वारा कुंजी टण्डन एम. ए चतुर्थ सेमेस्टर भूगोल अनुक्रमांक : 235070530009





क्षेत्रीय सर्वेक्षण प्रतिवेदन डोंगरगढ़ विकासखण्ड का आंगोलिक विश्तेषण एवं कृषि में नवाचार. चयनित ग्राम कातलवाही का प्रतीक अध्ययन सत्र 2023-2024 द्वारा किशन लाल एम०ए० चतुर्थ सेमेस्टर भूगोल अनुक्रमांक : 235070530008 नामांकन नं. - HU/515/19001100

प्राचार्या शासकीय नेहरू स्नातकोत्तर महाविद्यालय डोंगरगढ

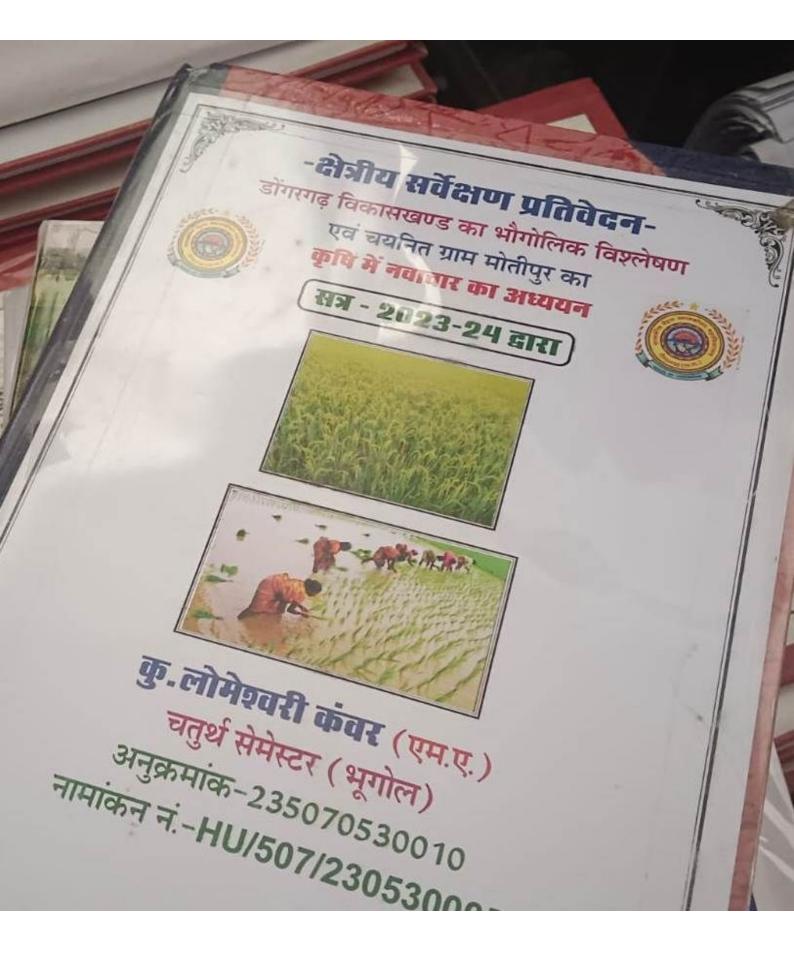
क्षेत्रीय सर्वेक्षण प्रतिवेदन

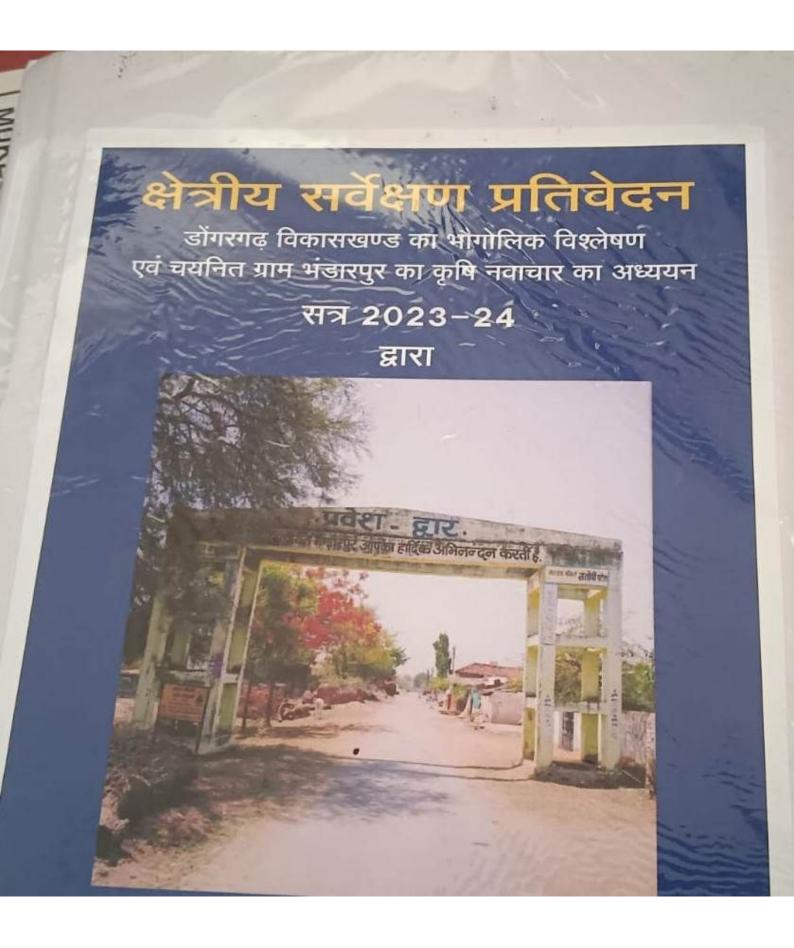
डोंगरगढ़ विकासखण्ड का भौगोलिक विश्लेषण एवं कृषि में नवाचार (चयनित ग्राम रीवागहन का प्रतीक अध्ययन)

सत्र 2023-2024









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This is to correly that project work entitled "ONLINE ELECTRICAL STORE" is carried out by CHERNDRAGEPT MANDAVDs student of PGDCA at GOVT. NEHRU PG COLLEGEE. DONGARDGARD is here by approved as a credible work in the discipline of computer science & Information Technology for the award of Post graduate diploma in computer application during the year 2023-2024 from Hemchand Yadav University, Durg (Clif.)

Principal

GOVT. NEHRU PG COLLEGE DONGARGARH

A

On

Mobile Shop

Submitted in partial fulfillment of the requirements for the award of

Post Graduate Diploma in Computer Application

From

Durg University Durg (C.G)

Year: 2023-24



Guided by :-Mrs. TulikaChakraborty

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Submitted by : -POONAM

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ONLINE TICKET BOOKING

On

Submitted in partial fulfilment of the requirements for the award of

Post Graduate Diploma in Computer Application

From

Hemchand Yadav University Durg (C.G.)

Year: 2023-24



Guided by: -MRS.TULIKA CHAKRABORTY

Submitted by : -JAMUNA BANJARE

Submitted to

GOVT. NEHRU PG COLLEGE DONGARGARH

Hemchand Yadav University Durg (C.G.)

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This is to certify that project work entitled "ONLINE SCHOOL MANAGEMENT" is carried out by KUNTI KHARE a student of PGDCA at GOVT. NEHRU PG COLLEGE DONGARGARH is here by approved as a credible work in the discipline of computer science & Information Technology for the award of Post graduate diploma in computer application during the year 2023-2024 from Hemchand Yadav University, Durg (C.G.).

> Principal Govt. Nebru P.G. Colleg. Principal Principal Brincipal College Dongargarh

Submitted to

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Durg University Durg (C.G)

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This is to certify that project work entitled "MOBILE SHOP" is carried out by POONAM a student of PGDCA at GOVT NEHRU PG COLLEGE

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Govt, Nehro P.G. College Dongargarh (C.G.)

Principal GOVT NEHRU PG COLLEGE DONGARGARH

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On

CAKE BAKERY SHOP

Submitted in partial fulfillment of the requirements for the award of

Post Graduate Diploma in Computer Application

From

Hemchand Yadav University Durg (C.G.)

Year: 2023-24



Guided by: -MRS.TULIKA CHAKRABORTY

Submitted by : -PRIYANKA DHURVE

Submitted to GOVT. NEHRU PG COLLEGE DONGARGARH Hemchand Yadav University Durg (C.G.)

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This is to certify that project work entitled "CS PHOTOGRAPHY" is carried out by CHANDAN SAHARE a student of PGDCA at GOVT NEHRU PG COLLEGE DONGARGARH is here by approved as a credible work in the discipline of computer science & Information Technology for the award of Post graduate diploma in computer application during the year 2023-2024fromDurg University, Durg(C.G.).

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This is to certify that project work entitled "BODY CARE BY HOME REMEDIES" is carried out by KHILESHWARI a student of PGDCA at GOVT. NEHRU PG COLLEGE DONGARGARH is here by approved as a credible work in the discipline of computer science & Information Technology for the award of Post graduate diploma in computer application during the year 2023-2024 from Hemchand Yadav University, Durg (C.G.).

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This is to certify that project work entitled "SHOP EASY" is carried out by PRAVIN SAHU a student of PGDCA at GOVT NEHRU PG COLLEGE DONGARGARH is here by approved as a credible work in the discipline of computer science & Information Technology for the award of Post graduate diploma in computer application during the year 2023-2024 from Durg University, Durg(C.G.).

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Project Report

On

BODY CARE BY HOME REMEDIES

Submitted in partial fulfillment of the requirements for the award of

Post Graduate Diploma in Computer Application

From

Hemchand Yadav University Durg (C.G.)

Year: 2023-24



Guided by: -MRS.TULIKA CHAKRABORTY

Submitted by : -KHILESHWARI

Submitted to

GOVT. NEHRU PG COLLEGE DONGARGARH Hemchand Yadav University Durg (C.G.)

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Project Report

On

M.K. PHOTOGRAPHY

Submitted in partial fulfillment of the requirements for the award of

Post Graduate Diploma in Computer Application

From

Hemchand Yadav University Durg (C.G)

Year: 2023-24



Guided by :-Mrs.Tulika Chakraborty

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Submitted by : -Mujahid Khan

Submitted to GOVT NEHRU PG COLLEGE DONGARGARH Hemchand Yadav University Durg (C.G)

On

CS PHOTOGRAPHY

Submitted in partial fulfillment of the requirements for the award of

Post Graduate Diploma in Computer Application

From

Hemchand Yadav University Durg (C.G)

Year: 2023-24



Guided by :-Mrs.Tulika Chakraborty

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Submitted by : -Chandan Sahare

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This is to certify that project work entitled "M.K. PHOTOGRAPHY" is carried out by Mujahid Khan a student of PGDCA at GOVT NEHRU PG COLLEGE DONGARGARH is here by approved as a credible work in the discipline of computer science & Information Technology for the award of Post graduate diploma in computer application during the year 2023-2024fromDurg University, Durg(C.G.).

ncipal Principal College

GOV TENEHRUPG COLLEGE DONGARGARH

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On

ONLINE SCHOOL MANAGEMENT

Submitted in partial fulfillment of the requirements for the award of

Post Graduate Diploma in Computer Application

From

Hemchand Yadav University Durg (C.G.)

Year: 2023-24



Guided by: -MRS.TULIKA CHAKRABORTY Submitted by : -KUNTI KHARE

Submitted to

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Hemchand Yadav University Durg (C.G.)

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This is to certify that project work entitled "CAKE BAKERY SHOP" is carried out by PRIYANKA DHURVE a student of PGDCA at GOVT. NEHRU PG COLLEGE DONGARGARH is here by approved as a credible work in the discipline of computer science & Information Technology for the award of Post graduate diploma in computer application during the year 2023-2024 from Hemchand Yadav University, Durg (C.G.).

Principal GOVT. NEHRU PG COLLEGE DONGARGARH

On

ONLINE ELECTRICAL STORE

Submitted in partial fulfillment of the requirements for the award of

Post Graduate Diploma in Computer Application

From

Hemchand Yadav University Durg (C.G.)

Year: 2023-24



Guided by: -MRS.TULIKA CHAKRABORTY Submitted by : -CHANDRAGUPT MANDA

Submitted to

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Website of college - http://www.gnpgcollege.in

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This is to certify that project work entitled "ONLINE TICKET BOOKING" is carried out by JAMUNA BANJAREa student of PGDCA at GOVT. NEHRU PG COLLEGE DONGARGARH is here by approved as a credible work in the discipline of computer science & Information Technology for the award of Post graduate diploma in computer application during the year 2023-2024 from Hemchand Yadav University, Durg (C.G.).

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